IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Halbe Hageman	§	Group Art Unit:	2111
Application No.	10/596,744	§	Examiner:	Auve, Glenn Allen
Filed:	03/05/2007	§	Confirmation No:	1023

Attorney Docket No: P19058-US1
Customer No.: 27045

For: Multisectional Bus In Radio Base Station And Method Of Using Such A Radio Base Station

Via EFS-Web

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

CERTIFICATE OF MAILING OR TRANSMISSION
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Date: April 5, 2012

Name: **Jennifer Hardin**

Signature: /Jennifer Hardin/

Request for Reconsideration of
Request for Certificate of Correction

The Applicant filed a Request for Certificate of Correction on January 28, 2010, requesting corrections to claims 9 and 10 of the issued patent; a copy of the request is submitted herewith in Appendix A. In response, the office issued a letter dated March 27, 2012, denying the request on the basis that the "[c]laims as issued are correct and in agreement with applicant's amendment of July 2, 2009" and that "[i]ssued claim 9 corresponds to claim 32 as filed and issued claim 10 corresponds to claim 31 as filed". Actually, issued claim 9 corresponds to original claim 31 and claim 20 corresponds to original claim 32; *i.e.,* the cancellation of claims 1-22 means that 22 was subtracted from each of the original claim numbers to arrive at the issued claim numbers. The problem, however, is not with the number of each of those claims, but with the internal references to other claims. Claim 31, **which was originally dependent from claim 23**, is correctly renumbered as claim 9 (31-22) in the issued patent but should be dependent

from claim 1, not claim 8 as appears in the issued patent; *i.e.*, 23-22 = 1. Similarly, claim 32, **which was originally dependent from claim 30**, is correctly renumbered as claim 10 (32-22) but should be dependent from claim 8, not claim 1 as appears in the issued patent; *i.e.*, 30-22 = 8.

Finally, the letter dated March 27, 2012, does not indicate why the third requested correction was denied. The requested correction is based on the fact that the last limitation of claim 10 is not present in the issued patent; *i.e.*, the last limitation of claim 10 is not "receiving task blocks from memory", but "maintaining said resource allocation table", which is present in the listing of claims presented in Applicant's last response to office action filed on July 2, 2009, a copy of which is submitted herewith as Appendix B.

Accordingly, the Applicant respectfully requests that the original Request for Certificate of Correction be approved and an appropriate certificate be issued.

Respectfully submitted,

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Date: April 5, 2012

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APPENDIX A

PTO/SB/44 (09-07)
Approved for use through 08/31/2010. OMB 0651-0033
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE
Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.
(Also Form PTO-1050)

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,624,220 B2

APPLICATION NO. : 10/596,744

ISSUE DATE : November 24, 2009

INVENTOR(S) : Hageman

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In Column 23, Line 50, in Claim 9, delete "claim 8," and insert - - claim 1, - -, therefor.

In Column 24, Line 4, in Claim 10, delete "claim 1, " and insert - - claim 8, - -, therefor.

In Column 24, Line 9, in Claim 10, delete "memory." and insert - - memory; maintaining said resource allocation table. - -.

MAILING ADDRESS OF SENDER (Please do not use customer number below):

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IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF: U.S. Patent No. 7,624,220

USPTO CONFIRMATION CODE: 1023

APPLICATION NO.: 10/596,744

PCT FILED: December 24, 2003

U.S. FILED: March 05, 2007

EXAMINER: Glenn A Auve

GROUP ART UNIT: 2111

FOR: MULTISECTIONAL BUS IN RADIO BASE STATION AND METHOD OF USING SUCH A RADIO BASE STATION

37 CFR 1.322 & 37 CFR 1.323 REQUEST FOR CERTIFICATE OF CORRECTION FOR USPTO AND/OR APPLICANT MISTAKE

HONORABLE COMMISSIONER OF PATENTS & TRADEMARKS

SIR:

The following is a request for a certificate of correction in Serial Number 10/596,744, now Patent Number 7,624,220.

A certificate of correction under 35 USC 254 is respectfully requested in the above-identified patent.

All errors were the fault of the USPTO, no fee required. In the event that a further fee is required, please charge the amount to our Deposit Account No. 50-1379.

The exact locations where the errors appear in the patent and patent application are as follows:

In Column 23, Line 50, in Claim 9, delete "claim 8," and insert - - claim 1, - -, therefor.
(AMENDMENTS TO THE CLAIMS DATED JULY 2, 2009, PAGE 3 OF 9, CLAIM 31, LINE 1)

In Column 24, Line 4, in Claim 10, delete "claim 1, " and insert - - claim 8, - -, therefor.
(AMENDMENTS TO THE CLAIMS DATED JULY 2, 2009, PAGE 3 OF 9, CLAIM 32, LINE 1)

In Column 24, Line 9, in Claim 10, delete "memory." and insert - - memory; maintaining said resource allocation table. - -.
(AMENDMENTS TO THE CLAIMS DATED JULY 2, 2009, PAGE 3 OF 9, CLAIM 32, LINE 7)

The requested corrections are attached on Form PTO 1050.

Respectfully Submitted

January 28, 2010

DATE

/SIDNEY L. WEATHERFORD,Reg#45602/

Sidney L. Weatherford
Registration No. 45602
Attorney of Record

APPENDIX B

Amendments to the Claims:

This listing of claims replaces all prior versions, and listings, of claims in the application:

Listing of Claims:

1-22. (Cancelled).

23. (Currently Amended) A communication system comprising a monitor, memory, a bus and ~~one or more~~ a plurality of resources, said memory being connected to the monitor via said bus and arranged for storing tasks and data, each of said resources being connected to the monitor via said bus and arranged for at least one of performing a function and executing a program, wherein said bus is implemented by a plurality of adjacent sections, each section being implemented as an ASIC connected to [[a]] at least one of said plurality of resources, said ASIC being arranged to assign sub busses of said bus with variable width.

24. (Previously Presented) Communication system according to claim 23, wherein said resources that are arranged to execute a program are also arranged to generate trigger signals and send them to the monitor, said monitor being arranged to receive said trigger signals, to read one or more tasks related to said trigger signals from said memory, to check whether resources required for performing said task are available and sending commands to selected resources specifying the task to be performed via said bus.

25. (Previously Presented) Communication system according to claim 23, wherein said resources are arranged for mutual communication via said bus.

26. (Previously Presented) Communication system according to claim 23, wherein using the bus is based on a datagram principle.

27. (Previously Presented) Communication system according to claim 23, wherein said memory comprises a task memory and a data memory.

28. (Previously Presented) Communication system according to claim 23, wherein said monitor comprises a state machine sequencer for handling several state machines in parallel.

29. (Previously Presented) Communication system according to claim 28, wherein said memory comprises a ROM portion and a RAM portion, said ROM portion storing state machine definitions for said state machine sequencer, task definitions and default structures, said RAM portion storing dynamic data.

30. (Previously Presented) Communication system according to claim 29, wherein said RAM portion stores a resource allocation table, a data block list, and data blocks.

31. (Previously Presented) Communication system according to claim 23, wherein said monitor comprises an executor arranged for:

sending commands to resources;
sending task block requests to memory;
receiving status information from resources;
receiving task blocks from memory.

32. (Previously Presented) Communication system according to claim 30, wherein said monitor comprises an executor arranged for:

sending commands to resources;.
sending task block requests to memory;
receiving status information from resources;.
receiving task blocks from memory;
maintaining said resource allocation table.

33. (Currently Amended) Communication system according to claim 23, wherein said resources comprises at least one of:

a transmitter,

a receiver,

an analogue signal manifold,

a digital to analogue converter,

an analogue to digital converter,

a control unit, and

a digital signal processor.

34. (Previously Presented) Communication system according to claim 33, wherein said resources comprise at least one digital signal processor storing an executable image for performing a program.

35. (Previously Presented) Communication system according to claim 23, wherein said communication system is a radio base unit.

36. (Previously Presented) Communication system according to claim 23, wherein each said ASIC comprises a bus control unit.

37. (Previously Presented) Communication system according to claim 23, wherein communications transmitted via said bus are multiplexed.

38. (Previously Presented) Communication system according to claim 23, wherein each said ASIC comprises a matrix structure with a plurality of cross points arranged to couple input lines with output lines.

39. (Previously Presented) Communication system according to claim 38, wherein said cross points are arranged to allow to isolate a group of input and output lines.

40. (Previously Presented) Communication system according to claim 38, wherein said cross points are arranged to allow to shift connections between input and output lines.

41. (Previously Presented) Communication system according to claim 23, wherein said bus is arranged on different boards that can be connected to one another.

42. (Currently Amended) Method of operating a communication system comprising a monitor, memory, a bus and ~~one or more~~ a plurality of resources, said memory being connected to the monitor via said bus and storing tasks and data, each of said resources being connected to the monitor via said bus, said bus being implemented by a plurality of adjacent sections, each section being implemented as an ASIC connected to [[a]] at least one of said plurality of resources, said method comprising:

assigning sub busses of said bus with variable width; and,

transmitting communications between said monitor, said memory and said ~~one or more~~ plurality of resources via said sub busses.

43-44. (Cancelled)

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